

**IN THE CLAIMS**

Claim 1 (Currently Amended): A semiconductor integrated circuit, comprising:

a shielded wire line; and

a shielding wire line provided for the shielded wire line, wherein a width of the shielding wire line is greater than and having a width broader than that of the shielded wire line, and each of the shielding wire line and the shielded wire line are positioned within a same layer of the semiconductor integrated circuit.